Okidata MLIB4T

	Switch Ba	ank 1	
р			
р			
е			
r	1	OFF	ODD PARITY
s	2	ON	EVEN PARITY
e r	3	ON	8 BITS
i a	4	ON	READY/BUSY
I	5	ON	CIRCUIT/MONITOR
B o	6	ON	PRINT MODE
a r	7	OFF	SSD
d	8	ON	DTR/RTS

Switch Ba	ank 2	
1	ON	
2	ON	1200 BAUD
3	OFF	▼
4	OFF	DSR INPUT SIGNAL
5	ON	32 BYTES BUFFER
6	ON	200ms BUSY SIGNAL
7	OFF	DTR SIGNAL
8	OFF	NOT USED

L	Switch Bank 1		
0			
w			
е			
r	1	ON	A
s	2	OFF	ASCII
е			—
r	3	OFF	
i	4	OFF	11 INCH FORM
a	-	0.1	
ı	5	ON	V
В	•	0 N	AUTO LINE EEED
0	6	ON	AUTO LINE FEED
a r	7	ON	8 DATA BITS
d	8	OFF	ENABLE FRONT PANEL